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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/635,717

08/05/2003

Yong-Deok Cho

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1935

8791

7590

06/16/2004

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EXAMINER

NGUYEN, MINH T

ART UNIT

PAPER NUMBER

2816

DATE MAILED: 06/16/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/635,717	CHO, YONG-DEOK	
	Examiner	Art Unit	
	Minh Nguyen	2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 2 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☒ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-5 is/are allowed.
- 6) ☐ Claim(s) ____ is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 August 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>8/5/03</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Specification

1. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Objections

2. Claims 1-2 and 4-5 are objected to because of the following informalities:

In claim 1, lines 10-11, the phrase “the buffered signal from the clock buffer” should be changed to -- a compensation clock signal --, see last line and Fig. 2,

lines 17 and 19, the terms “a dividing control signal” should be changed to -- the dividing control signal --, see line 7,

line 34, “external clock” should be changed to -- external clock signal -- for consistency, see line 2,

line 34, “internal clock” should be changed to -- internal clock signal --.

In claim 2, line 5, “a divided clock signal” should be changed to -- a plurality of divided clock signals --, see Fig. 3 and line 9,

line 9, “a plurality of divided clock signals” should be changed to -- the plurality of divided clock signals --,

line 10, “a reset signal” should be changed to -- the reset signal --, see line 5,

line 10, “an external part” should be changed to -- the external part --, see line 5,

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lines 14-15, "a plurality of synchronized" should be changed to -- the plurality of synchronized --, see line 13,

line 22, "a reset signal" should be changed to -- the reset signal --, see line 5.

In claim 4, lines 19, 27, 29 and 49, "forth" should be changed to -- fourth --,

lines 19-21, the terms "a first enable signal" should be changed to -- the first enable signal --,

line 43, "a" should be changed to -- the --.

In claim 5, the term "forth" should be changed to -- fourth --.

Appropriate correction is required.

Allowable Subject Matter

3. Claims 1-5 are allowed after the objections noted herein above are overcome.

Claims 1-5 are allowed because the prior art of record fails to disclose or suggest the inclusion of a DLL controller which receives the sample clock signal and the second comparison signal from the phase detector for generating the dividing control signal as recited in claim 1. The closest prior art of record, US Patent No. 6,005,426, teaches the principle of the present invention (Figs. 2A-2B); however, it does not teach the comparator (207) provides the sample clock signal and the second comparison signal to the DLL controller (203) as required in claim 1.

Conclusion

4. This application is in condition for allowance except for the formal matters noted above.

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Prosecution on the merits is closed in accordance with the practice under *Ex parte*

Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Minh Nguyen whose telephone number is 571-272-1748. The examiner can normally be reached on Monday, Tuesday, Thursday, Friday 7:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



6/10/04

Minh Nguyen
Primary Examiner
Art Unit 2816